Exhibit 3

BenQ – Conference Camera (see Product List at end for models) **Infringement of the ‘790 patent**	
Claim 1	**Evidence**
1. An interface for receiving data from an image sensor having an imaging array and a clock generator for transfer to a processor system comprising:	The BenQ conference camera has an interface for receiving data from an image sensor having an imaging array and a clock generator for transfer to a processor system. For example, an image capturing system of the conference camera has an image sensor (e.g. 2MP, 8MP CMOS sensor, depending on the camera model) that includes an imaging array and a clock generator. An interface system of the conference camera includes a processor system that performs operations on image data (e.g. H.264/MJPEG compression, image distortion correction – model dependent) and operations to output image data on an I/O interface (e.g. USB 2.0, USB 3.0, Ethernet). The interface system includes interface circuitry that receives image data from the image capturing system and transfers the image data to the processor system.
a memory for storing imaging array data and clocking signals at a rate determined by the clocking signals;	The BenQ conference camera has a memory for storing imaging array data and clocking signals at a rate determined by the clocking signals. For example, the interface circuitry includes a buffer module that stores the image data that is received from the image capturing system. The buffer module has data, control and clock signal inputs (e.g. to receive data, control and clock signalling output from the sensor’s I/O interface). The buffer module clocks its internal and external signals at a rate that is determined by the input clock signals. This enables the buffer module to store the image data at a rate that is in accordance with the pixel clock domain of the image capturing system.
a signal generator for generating a signal for transmission to the processor system in	The BenQ conference camera has a signal generator for generating a signal for transmission to the processor system in response to the quantity of data in the memory. For example, the interface circuitry includes interface functionality that

response to the quantity of data in the memory; and	generates a signal when the buffer module has image data that is ready for transmission to the processor system. The signal indicates that the buffer module has a frame of image data for the processor system.
a circuit for controlling the transfer of the data from the memory at a rate determined by the processor system.	The BenQ conference camera has a circuit for controlling the transfer of the data from the memory at a rate determined by the processor system. For example, the interface circuitry of the conference camera includes timing and control functionality that controls the transfer of image data from the buffer module to the processor system. The timing and control functionality enable the image data to be transferred at a rate determined by the processor system. This enables the processor system to acquire the image data at a rate that is in accordance with the processor clock domain.

Product List

DVY21 1080PMeeting Room Webcam
DVY22 4K Digital Zoom Conference Camera
DVY23 1080P PTZ Conference Camera
DVY31 Zoom™ Certified Full HD Business Webcam
DVY32 Zoom™ Certified Smart 4K UHD Conference Camera
VC01A 4K UHD Smart Video Bar

References

[1] DVY21/DVY22/DVY23
https://www.benq.com/content/dam/bb/ap/product/signage/Accessory/BenQ-webcam-brochure.pdf

[2] DVY32 Zoom™ Certified Smart 4K UHD Conference Camera
https://www.benq.com/en-us/business/signage/dvy32.html

[3] DVY31/DVY32/DVY23

https://www.benq.com/content/dam/bb/ap/product/signage/Accessory/dvy32/dvy31_dvy32_dvy23_camera_conference_datasheet.pdf

[4] VC01A 4K UHD Smart Video Bar
https://www.benq.com/content/dam/bb/ap/product/signage/Accessory/vc01a/vc01a-smart-video-bar-brochure.pdf